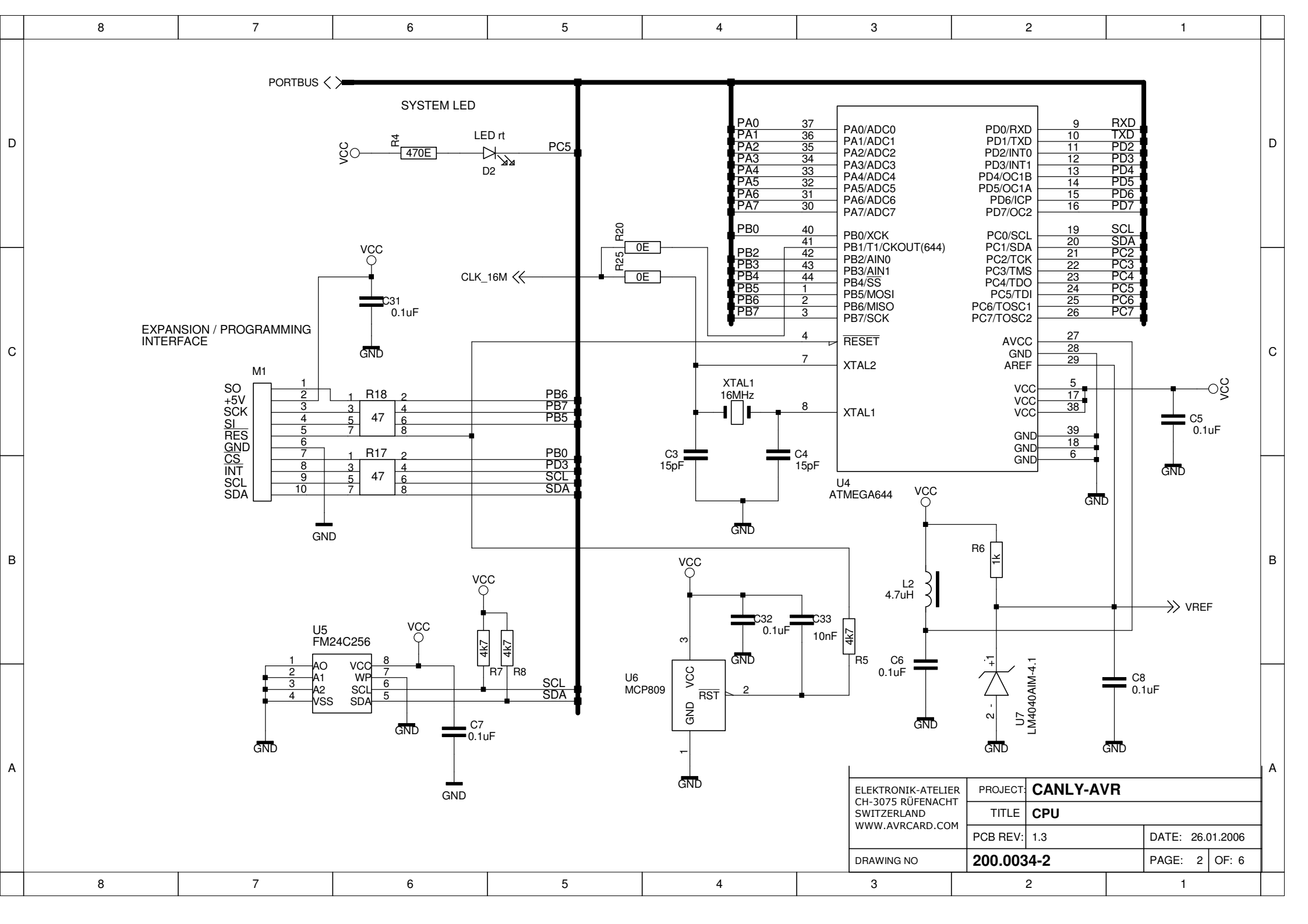
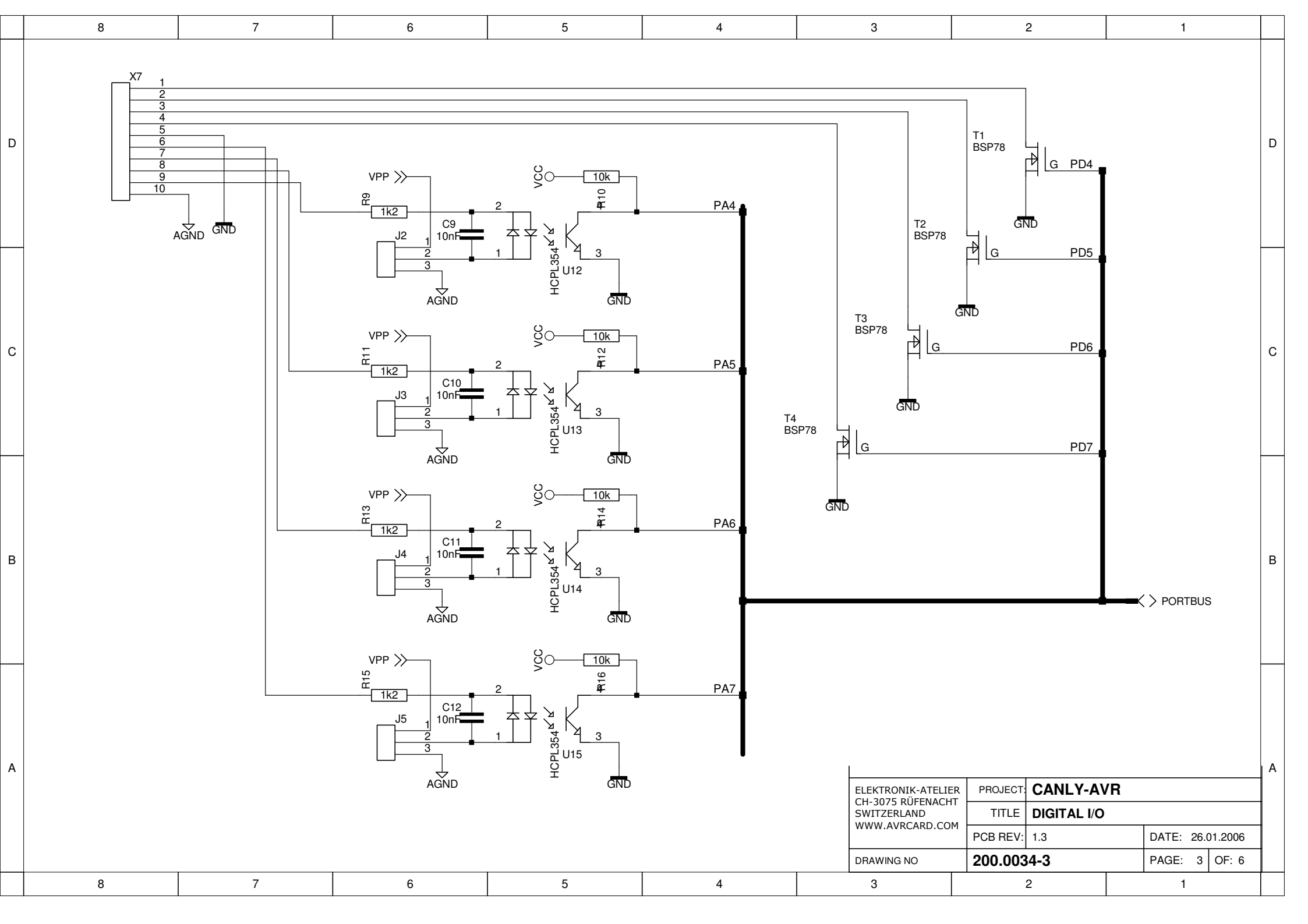


ACTVATE LINE TERMINATION

ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	<b>CANLY-AVR</b>	
	TITLE:	<b>CAN INTERFACE</b>	
	PCB REV:	1.3	DATE: 26.01.2006
DRAWING NO	<b>200.0034-1</b>		PAGE: 1 OF: 6



ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	<b>CANLY-AVR</b>		
	TITLE:	<b>CPU</b>		
PCB REV:	1.3	DATE:	26.01.2006	
DRAWING NO	<b>200.0034-2</b>	PAGE:	2	OF: 6



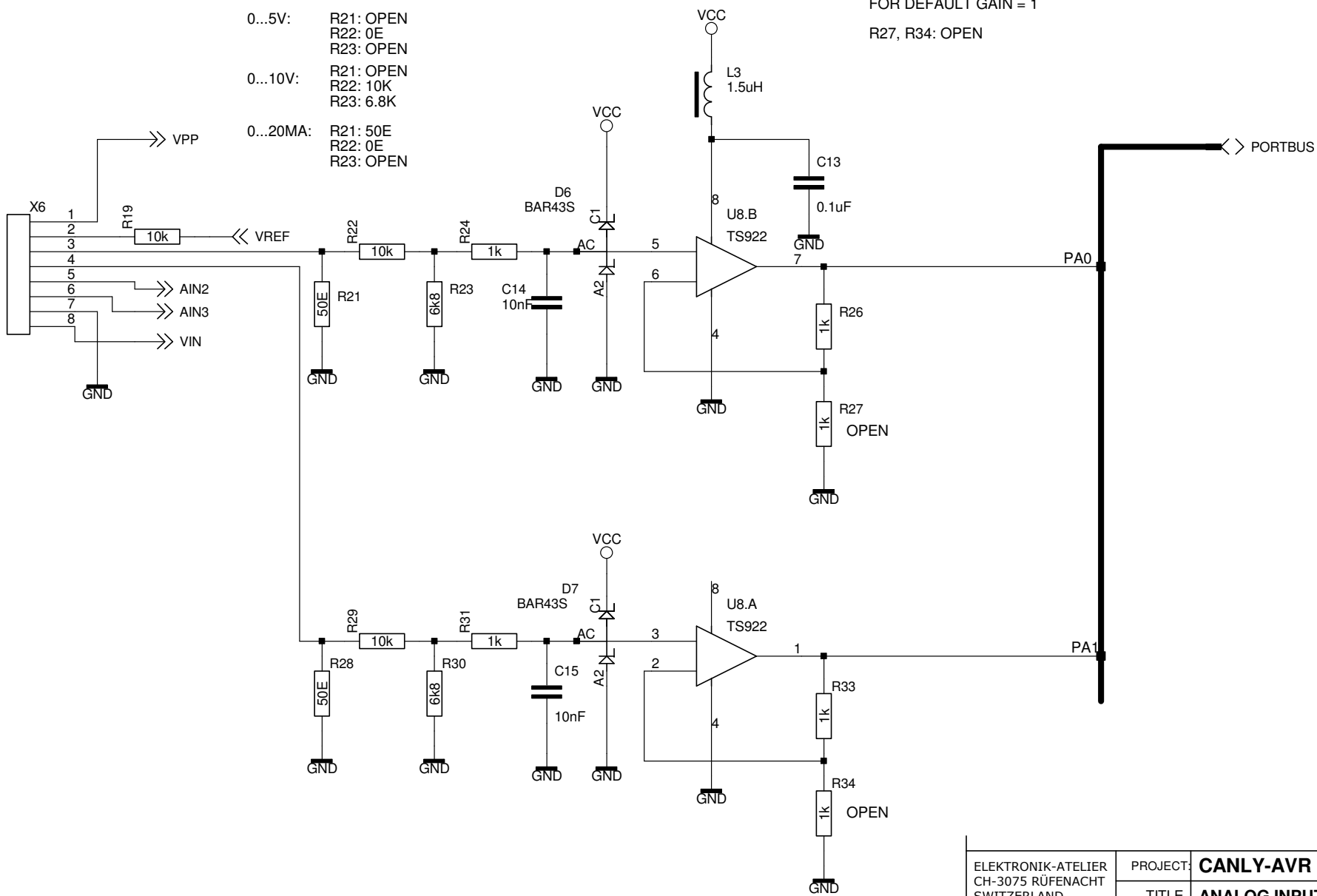
ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	<b>CANLY-AVR</b>	
	TITLE:	<b>DIGITAL I/O</b>	
	PCB REV:	1.3	DATE: 26.01.2006
DRAWING NO	<b>200.0034-3</b>		PAGE: 3 OF: 6

INPUT CONFIGURATION

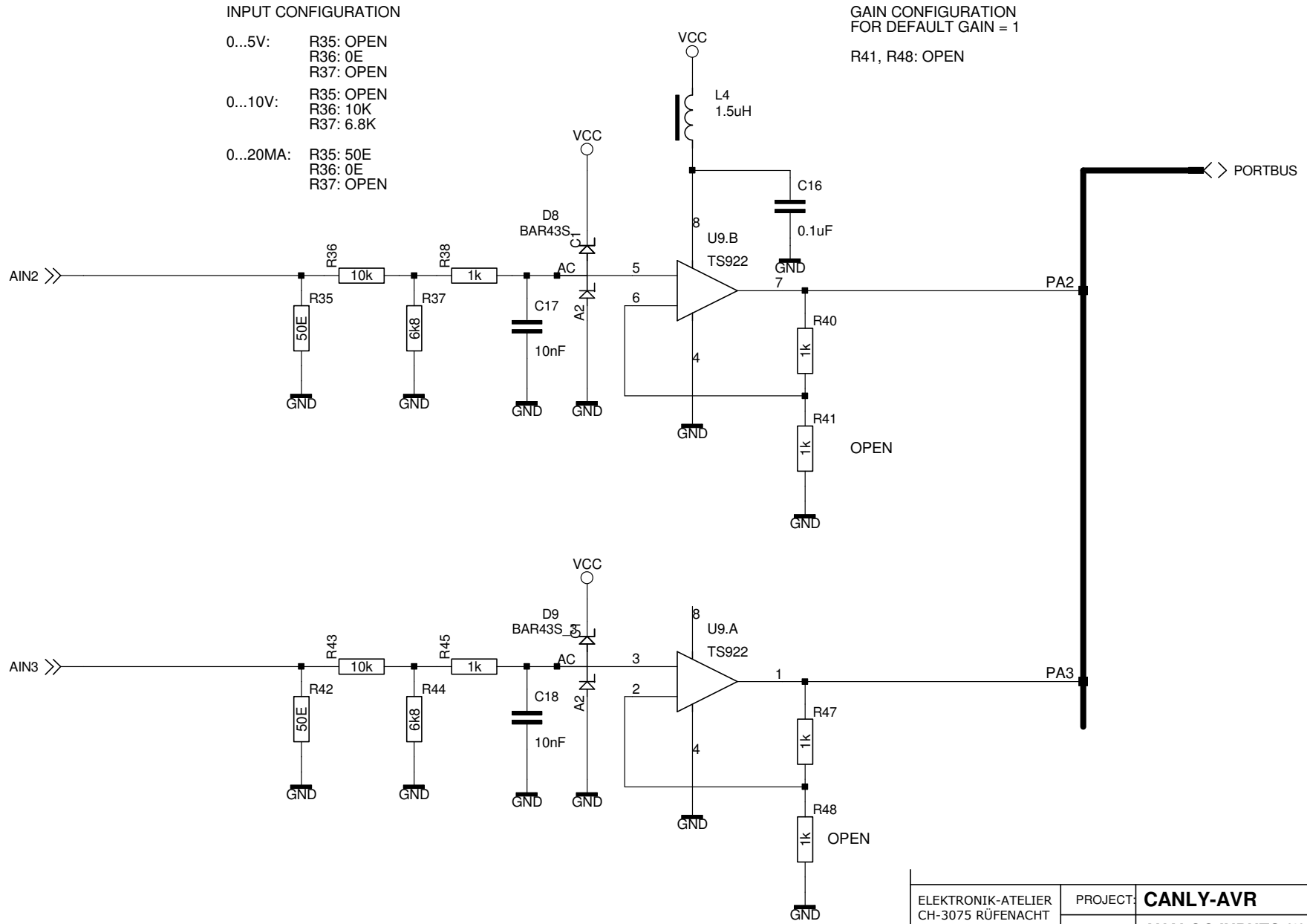
- 0...5V: R21: OPEN  
R22: 0E  
R23: OPEN
- 0...10V: R21: OPEN  
R22: 10K  
R23: 6.8K
- 0...20mA: R21: 50E  
R22: 0E  
R23: OPEN

GAIN CONFIGURATION  
FOR DEFAULT GAIN = 1

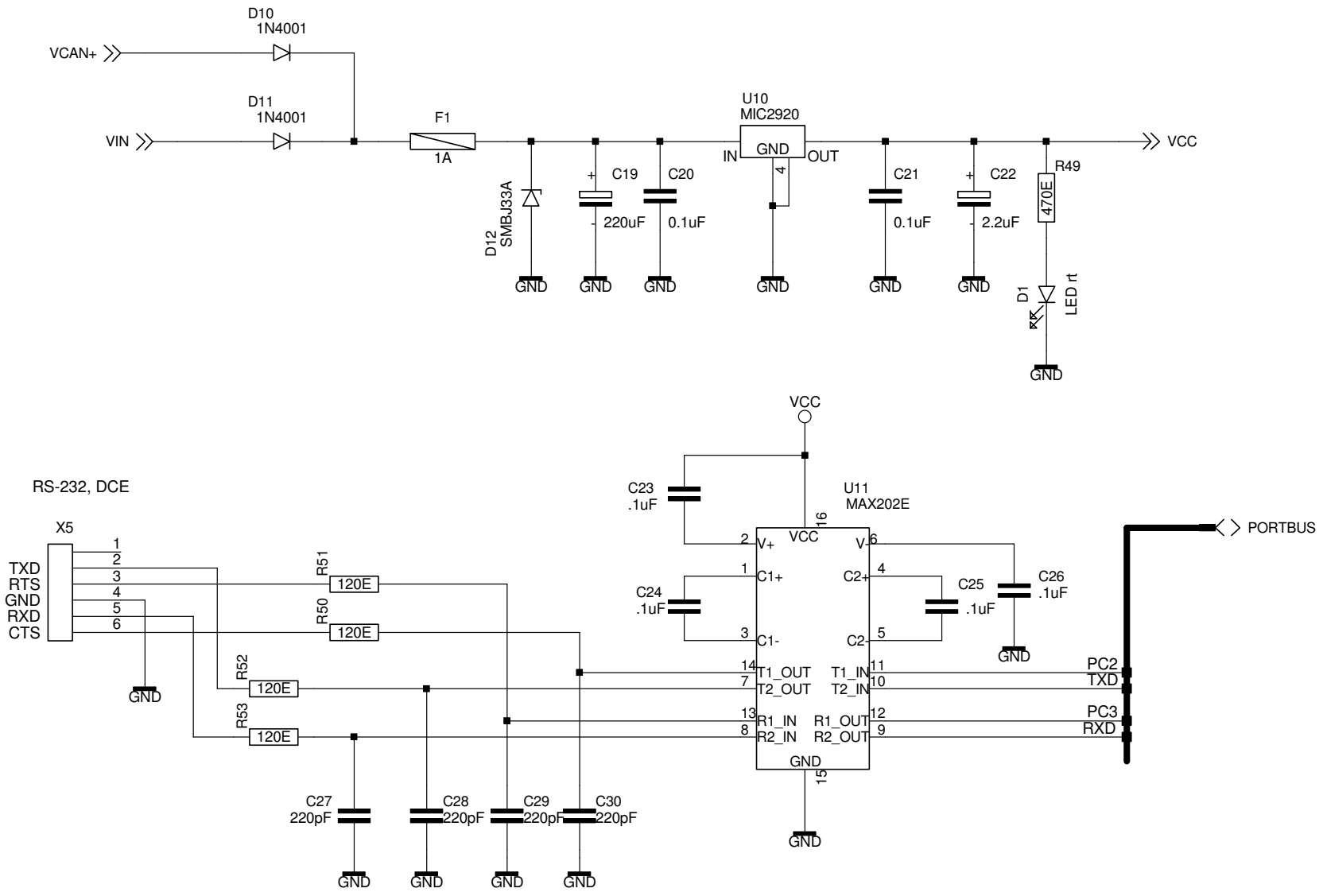
R27, R34: OPEN



ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	<b>CANLY-AVR</b>	
	TITLE:	<b>ANALOG INPUTS (1)</b>	
	PCB REV:	1.3	DATE: 26.01.2006
DRAWING NO	<b>200.0034-4</b>		PAGE: 4 OF: 6



ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	<b>CANLY-AVR</b>		
	TITLE:	<b>ANALOG INPUTS (2)</b>		
	PCB REV:	1.3	DATE:	26.01.2006
DRAWING NO	<b>200.0034-5</b>		PAGE:	5 OF: 6



ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	<b>CANLY-AVR</b>	
	TITLE:	<b>POWER / SERIAL</b>	
PCB REV:	1.3	DATE:	26.01.2006
DRAWING NO	<b>200.0034-6</b>		PAGE: 6 OF: 6