

ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	CANLY-AVR	
	TITLE:	CAN INTERFACE	
	PCB REV:	2.0	DATE: 06.07.2007
DRAWING NO	200.0078-1		PAGE: 1 OF: 6

D

D

C

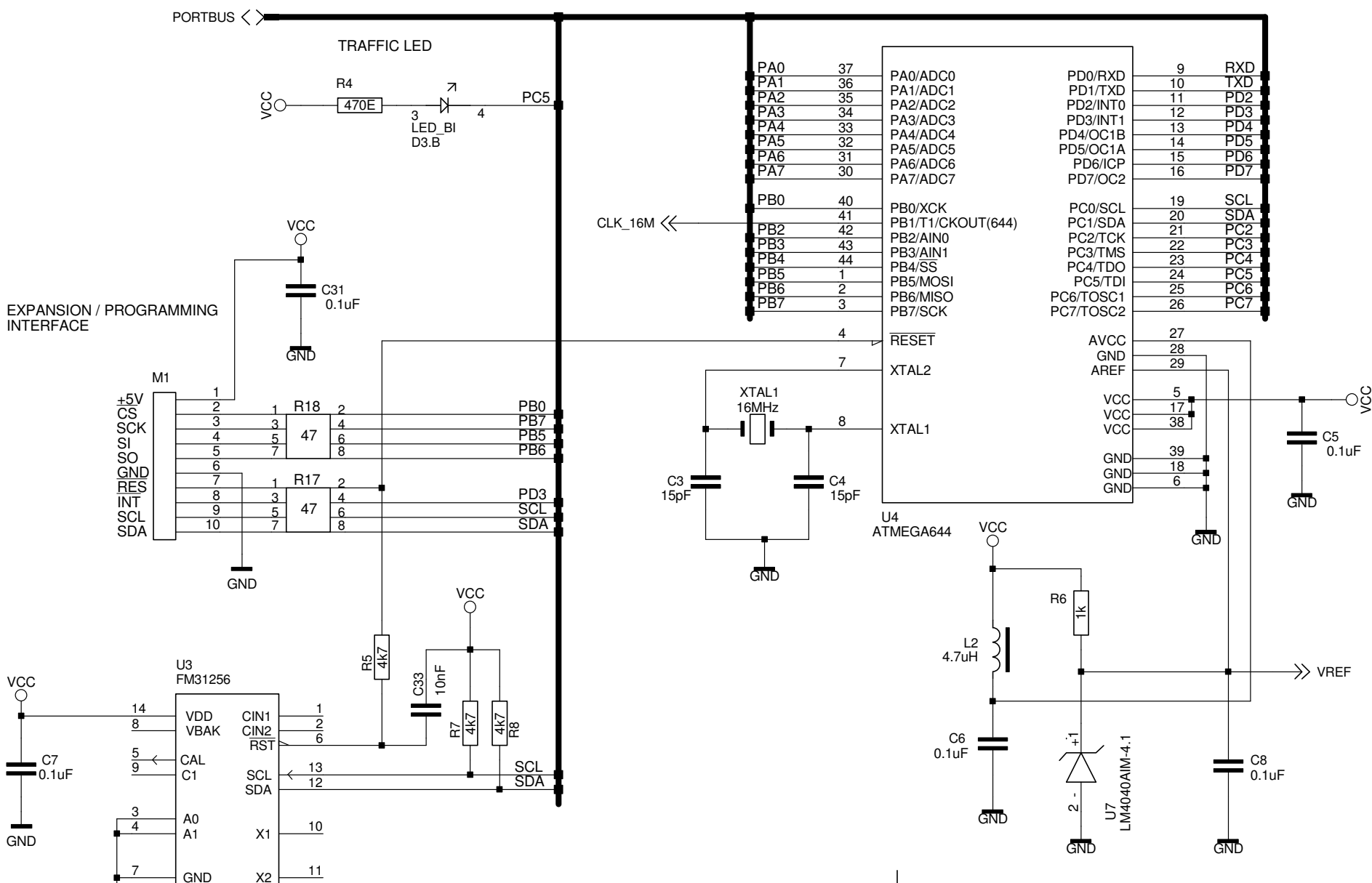
C

B

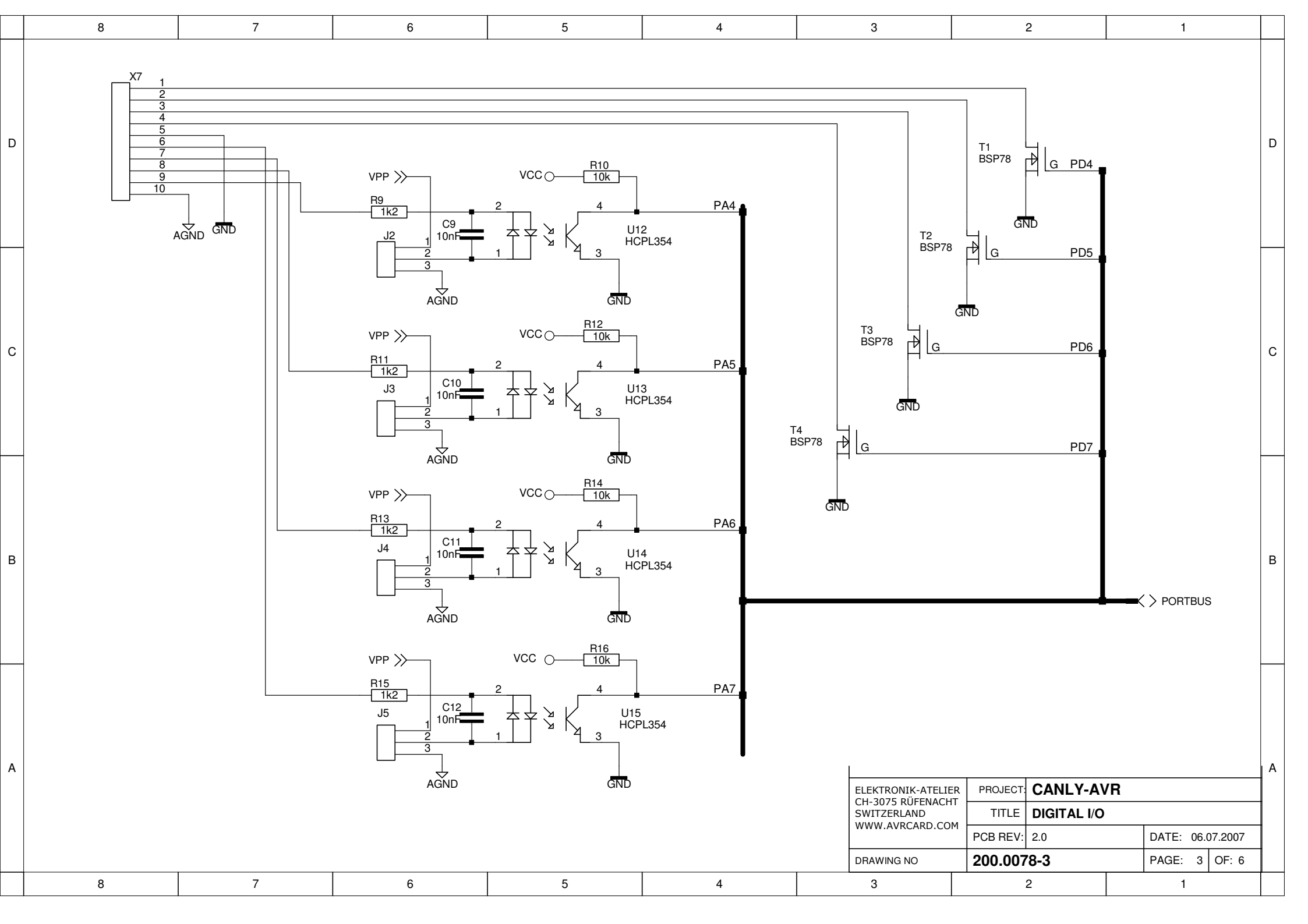
B

A

A



ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	CANLY-AVR	
	TITLE:	CPU	
DRAWING NO	PCB REV:	2.0	DATE: 06.07.2007
	200.0078-2		PAGE: 2 OF: 6



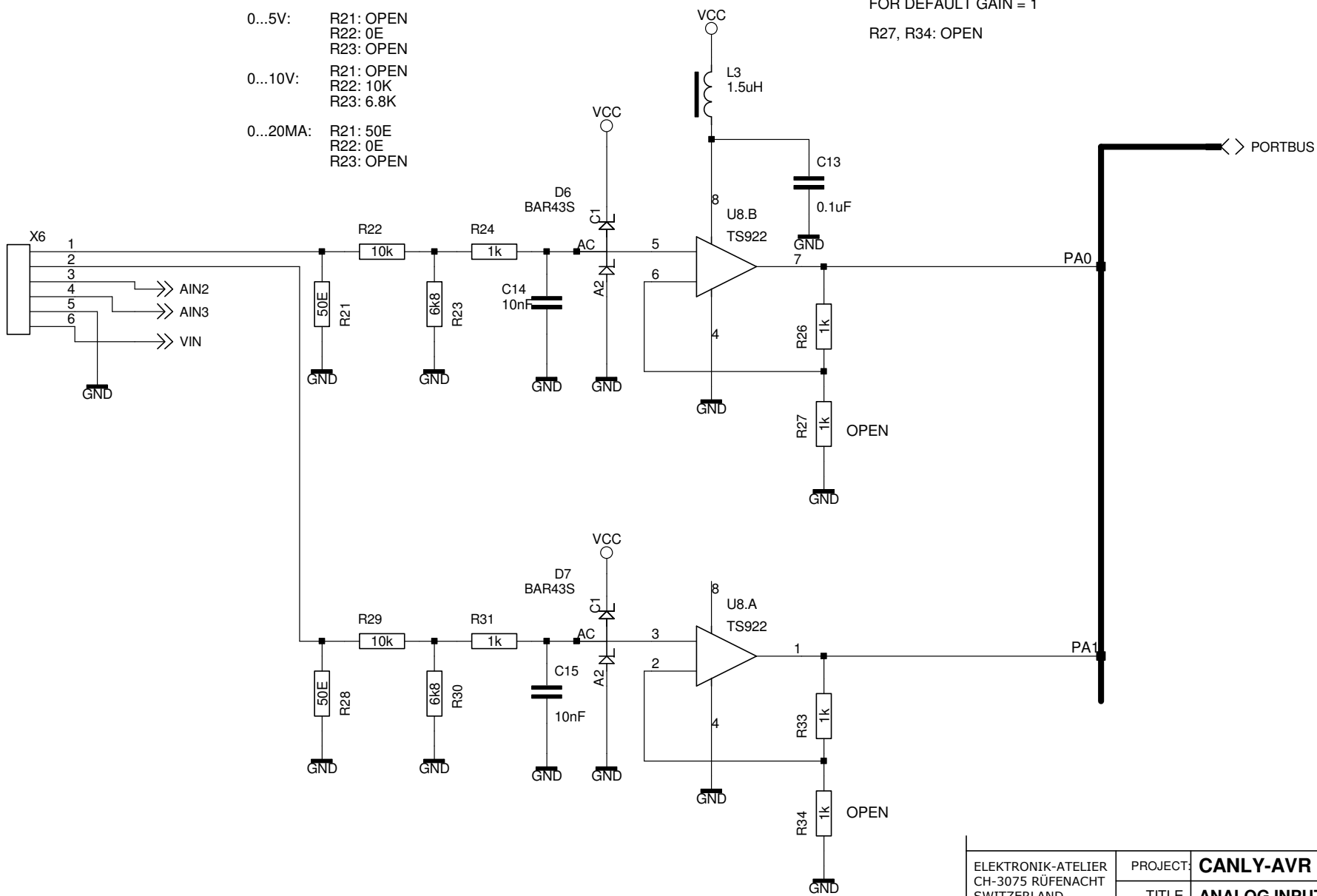
ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	CANLY-AVR	
	TITLE:	DIGITAL I/O	
PCB REV:	2.0	DATE:	06.07.2007
DRAWING NO	200.0078-3	PAGE:	3 OF: 6

INPUT CONFIGURATION

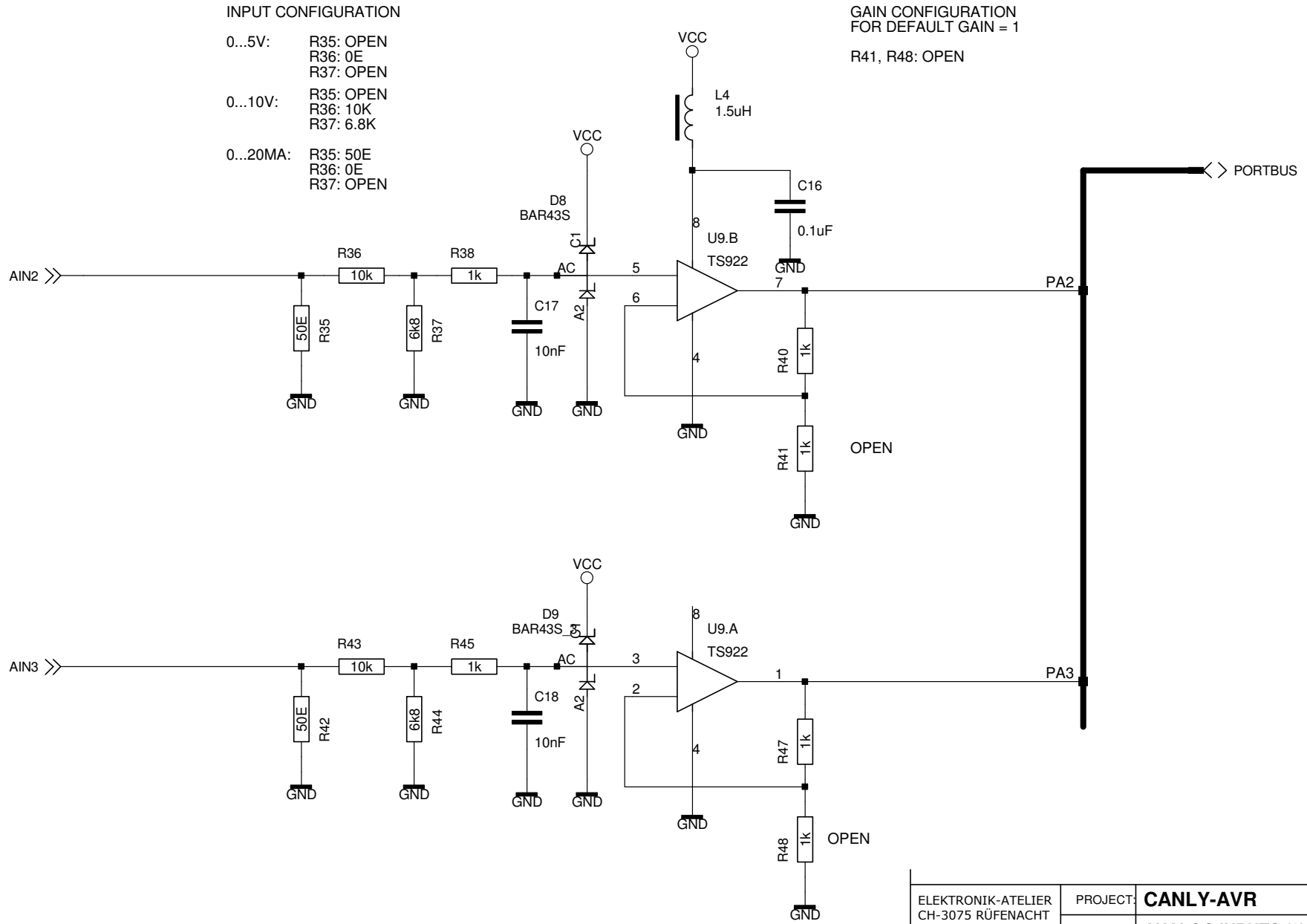
- 0...5V: R21: OPEN
R22: 0E
R23: OPEN
- 0...10V: R21: OPEN
R22: 10K
R23: 6.8K
- 0...20mA: R21: 50E
R22: 0E
R23: OPEN

GAIN CONFIGURATION
FOR DEFAULT GAIN = 1

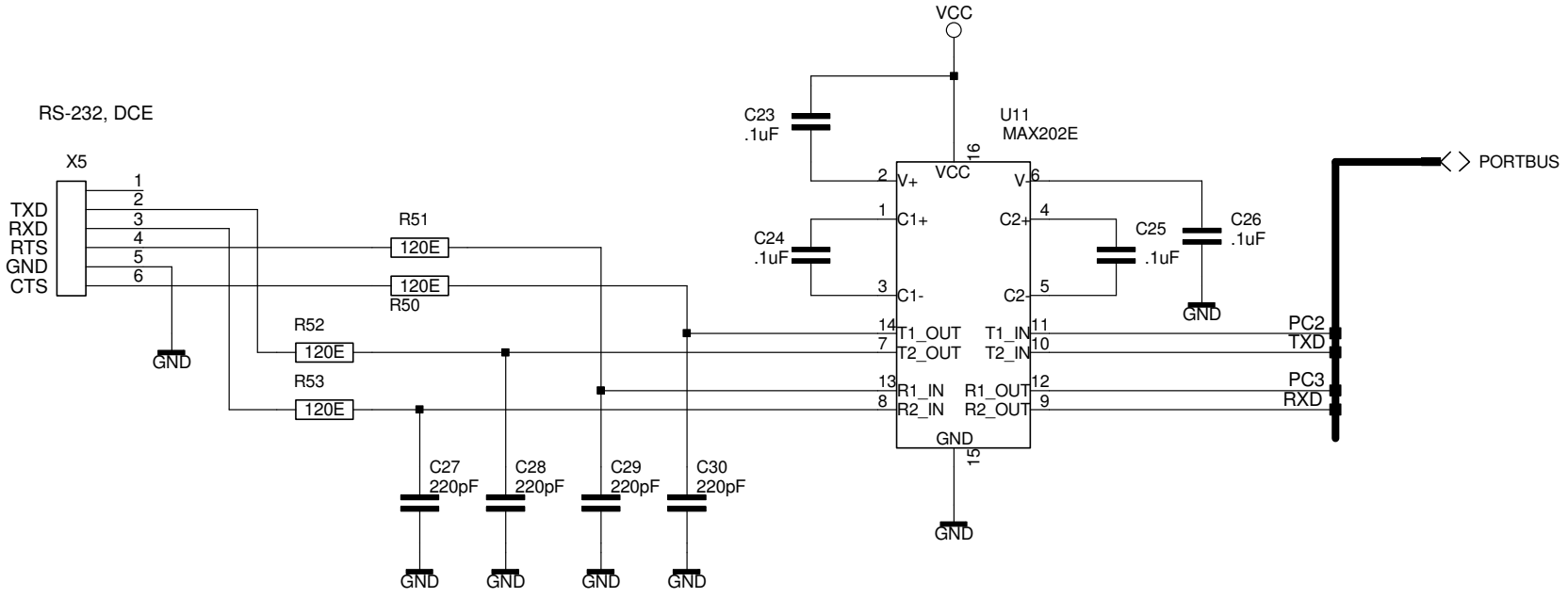
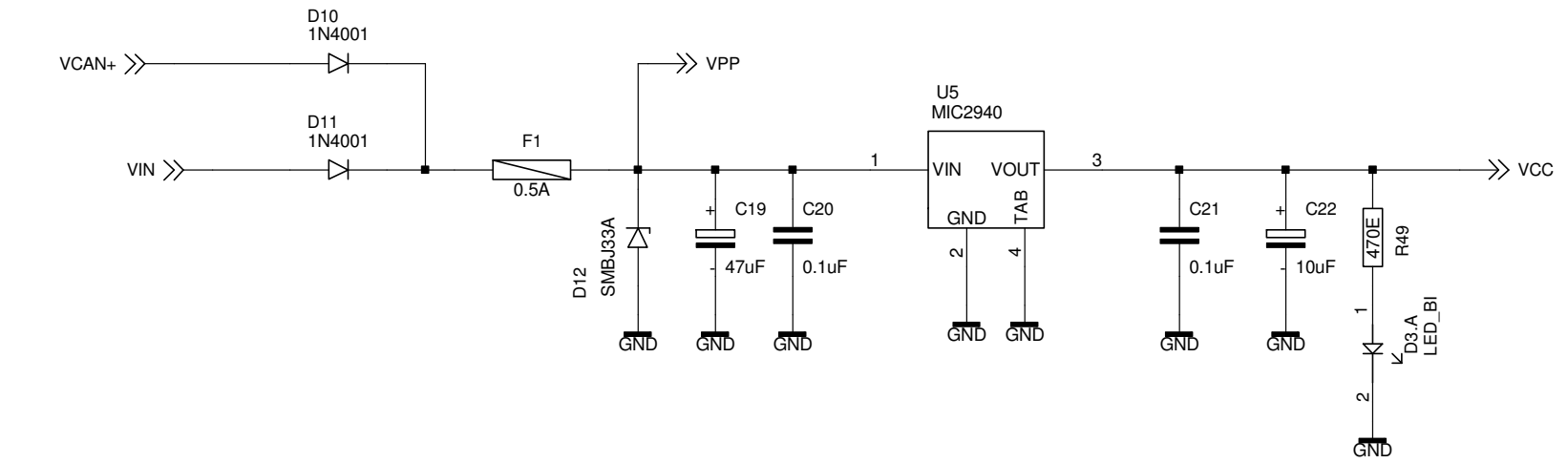
R27, R34: OPEN



ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	CANLY-AVR	
	TITLE:	ANALOG INPUTS (1)	
	PCB REV:	2.0	DATE: 06.07.2007
DRAWING NO	200.0078-4		PAGE: 4 OF: 6



ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	CANLY-AVR	
	TITLE:	ANALOG INPUTS (2)	
PCB REV:	2.0	DATE:	06.07.2007
DRAWING NO	200.0078-5		PAGE: 5 OF: 6



ELEKTRONIK-ATELIER CH-3075 RÜFENACHT SWITZERLAND WWW.AVRCARD.COM	PROJECT:	CANLY-AVR	
	TITLE:	POWER / SERIAL	
	PCB REV:	2.0	DATE: 06.07.2007
DRAWING NO	200.0078-6		PAGE: 6 OF: 6